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(54) Ballast for a discharge lamp

(57) A ballast for a discharge lamp comprises a smoothing capacitor (58), an inverter circuit (5) including at least one switching device (50,51) connected in parallel to the smoothing capacitor, a driving circuit (8) for driving the inverter circuit, an activating circuit (9) for activating the driving circuit, a resonance circuit (7) including first (58) and second (52) capacitors and an inductor (53) connected to output terminals of the inverter circuit, a discharge lamp (6) connected to output terminals of the resonance circuit, a third capacitor (54) connected

in parallel to the discharge lamp. A fourth capacitor (57) and first (58) and second (59) rectifying elements connected in series are connected to in parallel to the smoothing capacitor. Between both terminals of the fourth capacitor (57) and terminals for connection to an AC power source, a filter (2) and a rectifier (3) are inserted. A terminal of the first capacitor (58) other than that connected to the inverter circuit is connected to a junction of the first and second rectifying elements (58,59).

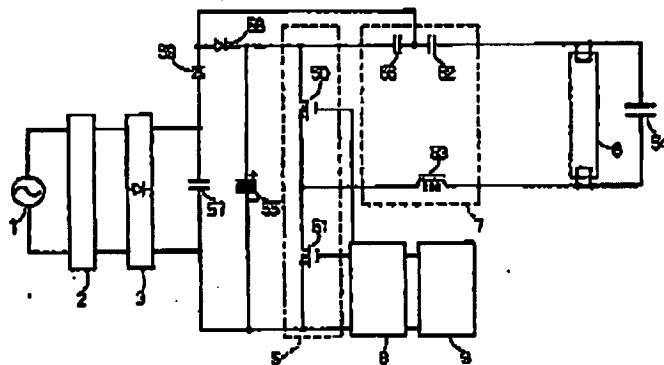


FIG. 1

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Description

[0001] The present invention relates to a ballast for a discharge lamp using an inverter circuit.

[0002] A circuit as shown in FIG. 11 is conventionally known as a ballast for a discharge lamp, where a chopper circuit 4 and an inverter circuit 5 share a switching device 51. This circuit is disclosed in Japanese Laid-Open Patent Publication (Tokukai-Hai) No. 8-276598 and is designed to have a high power factor as to an input voltage applied to a discharge lamp 6 and reduce harmonic components.

[0003] The circuit configuration of the ballast as mentioned above will be described specifically below. Referring to FIG. 11, transistors 50 and 51, which function as first and second switching devices respectively, are connected in series so as to constitute the inverter 5. The inverter 5 is connected in parallel to a smoothing capacitor 55. A driving circuit 8 is connected to the inverter circuit 5 to drive it. A resonance circuit 7 includes a capacitor 52 and an inductor 53 that are connected to the outputs of the inverter circuit 5. A discharge lamp 6 is connected to the output of the resonance circuit 7 and a capacitor 54 is connected in parallel to the discharge lamp 6. The output terminals of a rectifier 3, which is connected to an AC power source 1 via a noise filter 2, are connected to the source and the drain of the transistor 51 via the inductor 41.

[0004] Next, the operation of the above-described circuit will be described. An input from the AC power source 1 passes through the filter 2 and is subjected to full-wave rectification in the rectifier 3. The resultant ripple voltage is applied to the chopper circuit 4, so that the smoothing capacitor 55 is charged via the inductor 41. When a DC voltage is applied to the smoothing capacitor 55, the driving circuit 8 supplies a control signal so that the transistors 50 and 51 of the inverter circuit 5 alternate on and off repeatedly.

[0005] When the transistors 50 and 51 alternate on and off repeatedly in response to the control signal supplied from the driving circuit 8, an AC voltage is applied to the resonance circuit 7 including the capacitor 52 and the inductor 53 connected to the output terminals of the inverter circuit 5. As a result, a voltage and a current limited by the capacitor 52 or the inductor 53 are supplied to the discharge lamp 6 connected to the output terminal of the resonance circuit 7 so that the discharge lamp 6 lights. The capacitor 54 is connected in parallel to the discharge lamp 6 for the purpose of generating a voltage to start the discharge lamp 6 and stabilizing the lighting circuit.

[0006] In the above-described operation, when an instantaneous voltage of the AC power source 1 is low, the increase rate of the current that flows in the inductor 41 is low. Therefore, the energy accumulated in the inductor 41 for a fixed period is small when the transistor 51 turns off, so that the charging voltage to the smoothing capacitor 55 drops. At this time, the input current

becomes small in accordance with the instantaneous voltage of the AC power source 1.

[0007] When an instantaneous voltage of the AC power source 1 is high, the increase rate of the current that flows in the inductor 41 is high. Therefore, the energy accumulated in the inductor 41 for a fixed period is large when the transistor 51 turns off, so that the charging voltage to the smoothing capacitor 55 increases. The input current becomes large in accordance with the instantaneous voltage of the AC power source 1.

[0008] Therefore, input current with a waveform similar to the voltage waveform of the AC power source 1 can be obtained. Thus, the input power factor as to the power source can be high and the harmonic components of the input current from the power source can be reduced.

[0009] However, the conventional ballast described above poses a problem in that the configuration is complex so that it can result in a large-scale and expensive device. Furthermore, when a source voltage is applied to the ballast via a dimmer, the voltage of the smoothing capacitor is likely to drop so that the operation becomes unstable. For this reason, a dimmer is difficult to use with the device.

[0010] Therefore, in consideration of the foregoing, it is an object of the present invention to provide a ballast for a discharge lamp having a simple configuration that can start and maintain the lighting of a discharge lamp safely, and allows the use of a dimmer.

[0011] A ballast for a discharge lamp of the present invention comprises a smoothing capacitor, an inverter circuit including at least one switching device connected in parallel to the smoothing capacitor, a driving circuit for driving the inverter circuit, a resonance circuit including first and second capacitors and an inductor connected to output terminals of the inverter circuit, a discharge lamp connected to output terminals of the resonance circuit, and a third capacitor connected in parallel to the discharge lamp. Further a fourth capacitor and first and second rectifying elements connected in series are connected in parallel to the smoothing capacitor. Between both terminals of the fourth capacitor and terminals for being connected to an AC power source, a filter and a rectifier are inserted. A terminal of the first capacitor other than that connected to the inverter circuit is connected to a junction of the first and second rectifying elements.

[0012] It is preferable that the ballast further comprises an activating circuit for activating the driving circuit.

[0013] The first rectifying element is preferably a fast recovery diode, and the second rectifying element is preferably a fast recovery diode. The discharge lamp can be a preheat start type discharge lamp, and in that case the third capacitor is connected to the discharge lamp via a preheat electrode on the other side of the power source. A fifth capacitor is preferably connected in parallel to the discharge lamp.

[0014] Further, an inductor is preferably inserted between the fifth capacitor and the discharge lamp. The

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driving circuit for driving the inverter circuit preferably includes a transformer.

[0015] Further, in the case where a AC power source is connected to the device via a dimmer, the activating circuit preferably works at a voltage lower than a voltage at the filter caused as a result of voltage division between a capacitance of the dimmer and a capacitance of the filter. The smoothing capacitor preferably has a necessary breakdown voltage and a large capacitance. A fluorescent material having excellent persistence characteristics is preferably used in the discharge lamp. Preferably, a dimmer is further provided between the device and a commercial power source.

[0016] As described above, the ballast of the present invention has a simplified circuit configuration and allows input current to flow throughout all the cycles of a power source. The ballast has a high power factor as to input power source and can start and maintain lighting of a discharge lamp at reduced harmonic components of an input source current. Further the device allows dimming by using a dimmer for an incandescent lamp.

[0017] These and other advantages of the present invention will become apparent to those skilled in the art upon reading and understanding the following detailed description with reference to the accompanying figures.

BRIEF DESCRIPTION OF THE DRAWINGS

[0018] FIG. 1 is a circuit diagram of a ballast for a discharge lamp according to an embodiment of the present invention.

[0019] FIG. 2 is a circuit diagram of a ballast for a discharge lamp according to another embodiment of the present invention.

[0020] FIG. 3 is a circuit diagram of a ballast for a discharge lamp according to another embodiment of the present invention.

[0021] FIG. 4 is a circuit diagram of a ballast for a discharge lamp according to another embodiment of the present invention.

[0022] FIG. 5 is a circuit diagram of a ballast for a discharge lamp according to another embodiment of the present invention.

[0023] FIG. 6 shows a voltage waveform of a commercial power source.

[0024] FIG. 7 shows an output voltage waveform of a dimmer.

[0025] FIG. 8 shows a waveform of a discharge lamp current at a high degree of dimming.

[0026] FIG. 9 shows a waveform of output light from a discharge lamp at a high degree of dimming.

[0027] FIG. 10 shows an improved waveform of output light from a discharge lamp at a high degree of dimming.

[0028] FIG. 11 shows a circuit diagram of a conventional ballast for a discharge lamp.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0029] FIG. 1 is a circuit diagram of a ballast for a discharge lamp according to an embodiment of the present invention. In FIG. 1, the same circuit components as those in FIG. 11 bear the same reference numerals. The differences between the embodiment of FIG. 1 and the conventional device of FIG. 11 are as follows:

[0030] The device of FIG. 1 does not include the inductor 41 as shown in FIG. 11. A capacitor 58 is inserted between the output terminal of the inverter circuit 5 and the capacitor 62. First and second diodes 59 and 69 and a fourth capacitor 67 connected in series in that order are connected in parallel to the smoothing capacitor 55. The output terminals of the rectifier 3 is connected to the both terminals of the fourth capacitor 67. The junction of the first and second diodes 59 and 69 is connected to a junction of the capacitors 58 and 62. The operation of the ballast of FIG. 1 will be described below.

[0031] When power is supplied from the AC power source 1 to the rectifier 3 via the noise filter 2, it is full-wave rectified by the rectifier 3 and the resultant DC ripple voltage is supplied to the smoothing capacitor 55, so that the smoothing capacitor 55 is charged with the voltage. Upon application of the DC voltage to the smoothing capacitor 58, the activating circuit 9 works to activate the driving circuit 8. When the driving circuit 8 supplies a gate signal to the transistors 50 and 51 of the inverter circuit 5, the transistors 50 and 51 alternate on and off repeatedly.

[0032] The activating circuit 9 works as follows: When the smoothing capacitor 55 is charged by the voltage obtained by the full-wave rectification, the charged voltage is transmitted to the activating circuit 9 through stray capacitance and series or parallel resistance of the components (58, 62, 54, 53, 60 and 61) and the driving circuit 8, causing the activating circuit 9 to generate an activating signal. As means for transmitting the voltage applied from the rectifier 3, the activating circuit 9 can be actually connected to a junction located after the rectifier 3, such as the junction of the smoothing capacitor 55 and the diode 58, that of the diodes 59 and 69, or that of the diode 69 and the fourth capacitor 67.

[0033] Upon application of AC voltage to a resonance circuit 7 including the capacitors 56 and 52 and the inductor 53 connected to the output terminals of the inverter circuit 5, a voltage and a current limited by the capacitors 52 and 56 or the inductor 53 are supplied to a discharge lamp 6 connected to the output terminals of the resonance circuit 7. As a result, the discharge lamp 6 lights. The capacitor 64 is connected in parallel to the discharge lamp 6 for the purpose of generating a voltage for starting the discharge lamp 6 and stabilizing the operation of the lighting circuit.

[0034] In the starting and lighting operations of the discharge lamp, the DC voltage from the smoothing capacitor 55 is converted to a high frequency AC voltage.

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The high frequency AC voltage is generated by resonance current flowing through two closed loops alternately. One closed loop is constituted by the smoothing capacitor 55, the capacitors 56 and 52, the lamp 8 and the capacitor 54, the inductor 53 and the transistor 51. The other loop is constituted by the transistor 50, the inductor 53, the lamp 8 and the capacitors 54, 52 and 56.

[0035] The operation as described above is stable like a series inverter operation, which generally has been used in a conventional ballast with a low power factor. In the operation, the capacitor 55 is supplied with an AC voltage divided from resonance voltage, which has been divided between the capacitor 52 and the capacitor 56. Since the first diode 58 is connected in parallel to the capacitor 58, the AC voltage of the capacitor 56 is applied to the first diode 58. This voltage is superimposed on the charging path of the smoothing capacitor 55. Therefore, when the voltage at the cathode of the diode 58 is lower than that at the anode, the diode 58 is conducting so that the charging current flows from the rectifier 3 to the smoothing capacitor 55. When the voltage at the cathode of the diode 58 is higher than that at the anode, the diode 58 is out of conduction.

[0036] The first and second diodes 58 and 59 are inserted between the output terminal of the rectifier 3, i.e., the terminal of the fourth capacitor 57, and the terminal of the smoothing capacitor 55. When the voltage of the smoothing capacitor 55 becomes lower than the output voltage of the rectifier 3, the rectifier 3 supplies the charging current to the smoothing capacitor 55 via the first and second diodes 58 and 59. In this manner, the smoothing capacitor is charged by the effect of pulling the charging current throughout all the cycles of the AC power source.

[0037] The second diode 59 is inserted for the purpose of suppressing the influence of the fourth capacitor 57 on the resonance circuit 7. The effect of pulling the high frequency current from the rectifier 3 can be brought, as described above, by selecting a capacitance that causes a ripple in full-wave rectified voltage waveform as a capacitance of the fourth capacitor 57. Thus, the pulled current at a low wave height of the commercial power source can be increased. Furthermore, since the fourth capacitor 57 is connected in parallel to the smoothing capacitor 55 having a large internal resistance via the first and second diodes 58 and 59, when large current is needed, for example, to start the discharge lamp 6, the combined resistance of the resonance circuit drops so that a larger starting voltage can be applied to the discharge lamp 6.

[0038] The high frequency charging current is averaged by the filter 2. Thus, a part of the resonance voltage is fed back between the rectifier 3 and the smoothing capacitor 55 while the DC voltage of the smoothing capacitor 55 is converted to a high frequency AC voltage to light the discharge lamp 6, so that the smoothing capacitor 55 can be charged with high frequency. As a result, input current flows throughout all the cycles, so that

the power factor is high, and the input current results in a sine wave with a small THD (total harmonic distortion).

[0039] Furthermore, the ballast of the present invention can be used with a dimmer for an incandescent lamp without problems. The dimmer for an incandescent lamp performs phase control, primarily using a triac, and therefore in a circuit with a low power factor, the dimmer is prone to malfunction because the triac is not brought into normal conduction. On the other hand, the ballast of the present invention has a high power factor, and the input current flows without interruption in the same manner as in the incandescent lamp. Therefore, even if the ballast of the present invention is connected with a dimmer, no malfunction occurs. Since the smoothing capacitor 55 is charged with a voltage increased by resonance, the discharge lamp 6 can light stably even under a high degree of dimming.

[0040] A fast recovery diode can be used for the first diode 58 to ensure the effect of the resonance current at the resonance circuit on the charging path for the smoothing capacitor 55. Furthermore, the heat generation of the diode 58 itself can be reduced.

[0041] A fast recovery diode is preferably used for the second diode 59 as well. This leads to reduced influence of the resonance current and a higher power factor and a lower THD. Furthermore, the heat generation of the diode 59 itself can be reduced, thus contributing to high efficiency.

[0042] When the discharge lamp 6 is of a preheat start type, as shown in FIG. 2, the third capacitor 54 is preferably connected to the discharge lamp 6 on the side opposite to the power source via a preheat electrode. This makes it possible to prevent the increase of the discharge lamp voltage that otherwise may be required to be increased in response to the drop of the temperature of the preheat electrode when the preheat start type discharge lamp is dimmed, i.e., operated with low current. Thus, the wear of the preheat electrode and a discharge auxiliary, i.e., an emitter, can be reduced and the lifetime can be prolonged.

[0043] As shown in FIG. 3, a fifth capacitor 62 can be further connected in parallel to the discharge lamp 6 on the side of the power source so that the fifth capacitor 62 supplies a voltage for maintaining discharge. This embodiment makes it possible to avoid a non-discharge state, i.e., a preheat state at a high degree of dimming, i.e., during lighting with low illumination. Therefore, this embodiment is particularly advantageous when the degree of dimming is such that an optical output becomes lower than 10% of the rated optical output.

[0044] As shown in FIG. 4, an inductor 80 can be inserted between the fifth capacitor 62 and the discharge lamp 6 so that the surge of the discharge lamp current, as shown in FIG. 8, at a high degree of dimming, i.e., during lighting with low illumination, is reduced. As a result, the wear of the electrode can be suppressed, so that the lifetime is prolonged, and variations in the optical output can be reduced.

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[0045] As shown in FIG. 5, the driving circuit 8 for driving the inverter circuit preferably includes a transformer 68. The relations among sources of operation power supply and paths of operation current are complicated in each phase of the commercial power source and each phase of high frequency operations. Therefore smooth control can be achieved with a simplified driving circuit with the transformer 68 rather than a separate excitation type driving circuit that performs complex control.

[0046] In FIG. 5, the transformer 68 is inserted between the output of the inverter circuit and the inductor 63. However, the transformer 68 can be inserted between the inductor 63 and the discharge lamp 6. Alternatively, the transformer 68 can be inserted in series with the third capacitor 54. In short, the transformer 68 is inserted such that high frequency current can be picked up. Furthermore, when the transformer 68 is provided with separate excitation control, the input current waveform can be formed more precisely.

[0047] When the ballast is used with a dimmer, it is preferable to design the circuit such that the activating circuit 9 operates to activate the driving circuit 8 at a voltage lower than a voltage generated at both terminals of the filter 2 as a result of dividing voltage between the capacitance of the dimmer and the capacitance of the filter 2. When the ballast is connected to a commercial power source with a sine wave as shown in FIG. 6, a voltage applied to the filter 2 of the ballast is generated as a result of dividing the voltage shown in FIG. 6 between the capacitance of the dimmer and the capacitance of the filter 2, before the ballast is activated.

[0048] Generally, when the dimming degree is small, no problem is caused because the effective voltage is large. On the other hand, when the dimming degree is large, the effective voltage is small, as shown in FIG. 7. Therefore, the current through the dimmer and the ballast is small so that the ballast does not operate normally. However, it is possible to start an operation at a low discharge current by designing the device such that the activating circuit 9 can operate at a voltage lower than the divided voltage.

[0049] For example, when the capacitance at both terminals of an external dimmer is $0.15 \mu\text{F}$, the capacitance of the filter is $0.1 \mu\text{F}$, and the voltage of a power source is 120V, a voltage of 72V is applied to the filter, i.e., the ballast, before the activating circuit operates. The voltage is calculated as follows: $120 \times 0.15 / (0.15 + 0.1) = 72 \text{ (V)}$. Therefore the ballast may be designed such that the activating circuit 9 can operate at a voltage of 72V or less.

[0050] Furthermore, the smoothing capacitor 55 preferably has a breakdown voltage higher than 1.3 times of that applied during lighting by rated voltage. Also it preferably has a capacitance such that a ratio of peak value to root mean square value of a lamp current during lighting by a voltage of 80% of rated voltage becomes lower than 1.7. The larger capacitance is the more preferable. By virtue of such conditions, it is possible to re-

duce the heat generation of the smoothing capacitor 55 where high frequency charging current flows. As a result, inexpensive capacitors can be used rather than components with a high temperature rating or a high current rating.

[0051] A fluorescent material having excellent persistence characteristics is preferably used for the discharge lamp. As shown in FIG. 9, current that contributes to emission of light is mostly supplied by the commercial power source. By using a fluorescent material having such excellent persistence characteristics that the optical output is reduced to a half in 1.5 milliseconds or more, flicker can be reduced at the frequency of the commercial power source, as shown in FIG. 10, even if the ballast is used with a dimmer.

[0052] As described above, the present invention provides a ballast for a discharge lamp with a simplified circuit configuration that allows input current to flow throughout all the cycles, can start to light a discharge lamp and maintain the lighting at reduced harmonic components of the input current from the power source, and allows dimming with a dimmer.

[0053] In the embodiments described above, a series inverter system where two transistors are connected in series is used. However, inverters of other systems such as a push-pull inverter can be used. Alternatively, a single-transistor type inverter can be used. Furthermore, the driving circuit 8 may be a separate excitation type including a separate excitation circuit, or a self-excitation type using a feedback signal from the resonance circuit 7. Alternatively, a driving circuit in combination of a separate excitation type and a self-excitation type can be used.

[0054] In the embodiments described above, the series resonance circuit comprising the capacitor and the inductance is used. However, a parallel resonance circuit can be used. Furthermore, the switching device is not limited to an FET, but other switching devices such as bipolar transistors may be used. The discharge lamp is not limited to a preheat start type discharge lamp, but a cold cathode start type discharge lamp, a low-pressure discharge lamp or a high-pressure discharge lamp may be used. Furthermore, the power source is not limited to a commercial power source, but other AC power sources (whose frequency is not either 60Hz or 50Hz) may be used.

[0055] The invention may be embodied in other forms without departing from the essential characteristics thereof. The embodiments disclosed in this application are to be considered in all respects as illustrative and not limiting. The scope of the invention is indicated by the appended claims rather than by the foregoing description, and all changes which come within the meaning and range of equivalency of the claims are intended to be embraced therein.

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Claims

1. A ballast for a discharge lamp, comprising:

a smoothing capacitor (55);
 an inverter circuit including at least one switching device (50,51) connected in parallel to said smoothing capacitor;
 a driving circuit (8) for driving said inverter circuit;
 a resonance circuit including first (56) and second (52) capacitors connected in series and an inductor (53) connected to output terminals of said inverter circuit;
 a discharge lamp (5) connected to output terminals of said resonance circuit;
 a third capacitor (54) connected in parallel to said discharge lamp;
 a fourth capacitor (57) and first (58) and second (59) rectifying elements connected in series, which are connected in parallel to said smoothing capacitor (55); and
 a filter (2) and a rectifier (3) inserted between both terminals of said fourth capacitor (57) and terminals for connection to an AC power source;
 wherein a terminal of said first capacitor (56) other than that connected to said inverter circuit is connected to a junction of said first and second rectifying elements (58,59).

2. A ballast for a discharge lamp according to claim 1, further comprising an activating circuit (9) for activating said driving circuit.

3. A ballast for a discharge lamp according to claim 1 or 2, wherein said driving circuit includes a transformer (89).

4. A ballast for a discharge lamp according to claim 1, 2 or 3, wherein said first rectifying element (58) is a fast recovery diode.

5. A ballast for a discharge lamp according to claim 1, 2, 3 or 4, wherein said second rectifying element (59) is a fast recovery diode.

6. A ballast for a discharge lamp according to any preceding claim, wherein

said discharge lamp (5) is of a preheat start type, and
 said third capacitor (54) is connected to said discharge lamp via a preheat electrode on a side opposite to the AC power source.

7. A ballast for a discharge lamp according to claim 6, wherein a fifth capacitor (62) is connected in parallel

to said discharge lamp on a side closer to the power source.

8. A ballast for a discharge lamp according to claim 7, wherein an inductor (60) is inserted between said fifth capacitor (62) and said discharge lamp.

9. A ballast for a discharge lamp according to any preceding claim, wherein
 said activating circuit operates at a voltage not more than a voltage generated in the filter as a result of dividing voltage between a capacitance of a dimmer and a capacitance of said filter, when the ballast is connected to an AC power source via the dimmer.

10. A ballast for a discharge lamp according to any preceding claim, wherein a fluorescent material used in said discharge lamp has persistence characteristics such that the optical output is reduced to a half in 1.8 milliseconds or more.

11. A ballast for a discharge lamp according to any preceding claim, further comprising a dimmer connected between said discharge lamp and an AC power source.

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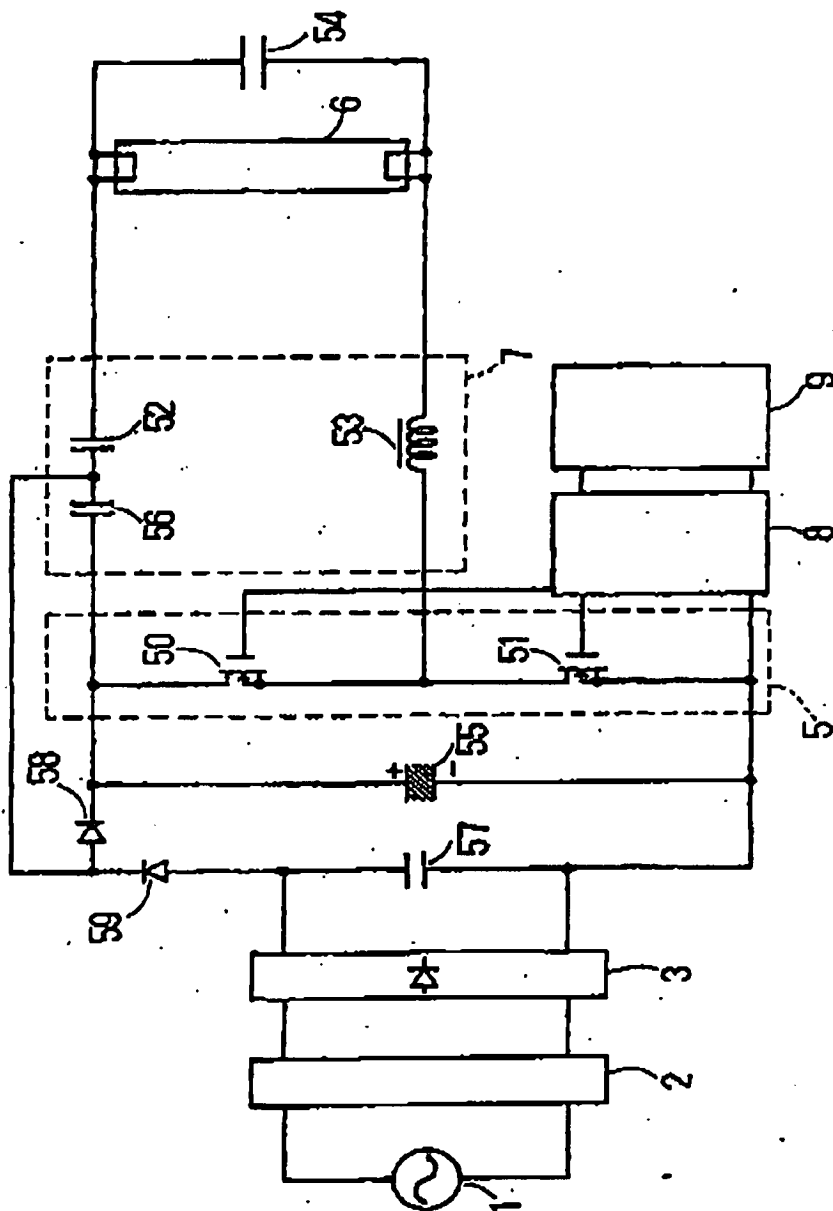


FIG. 1

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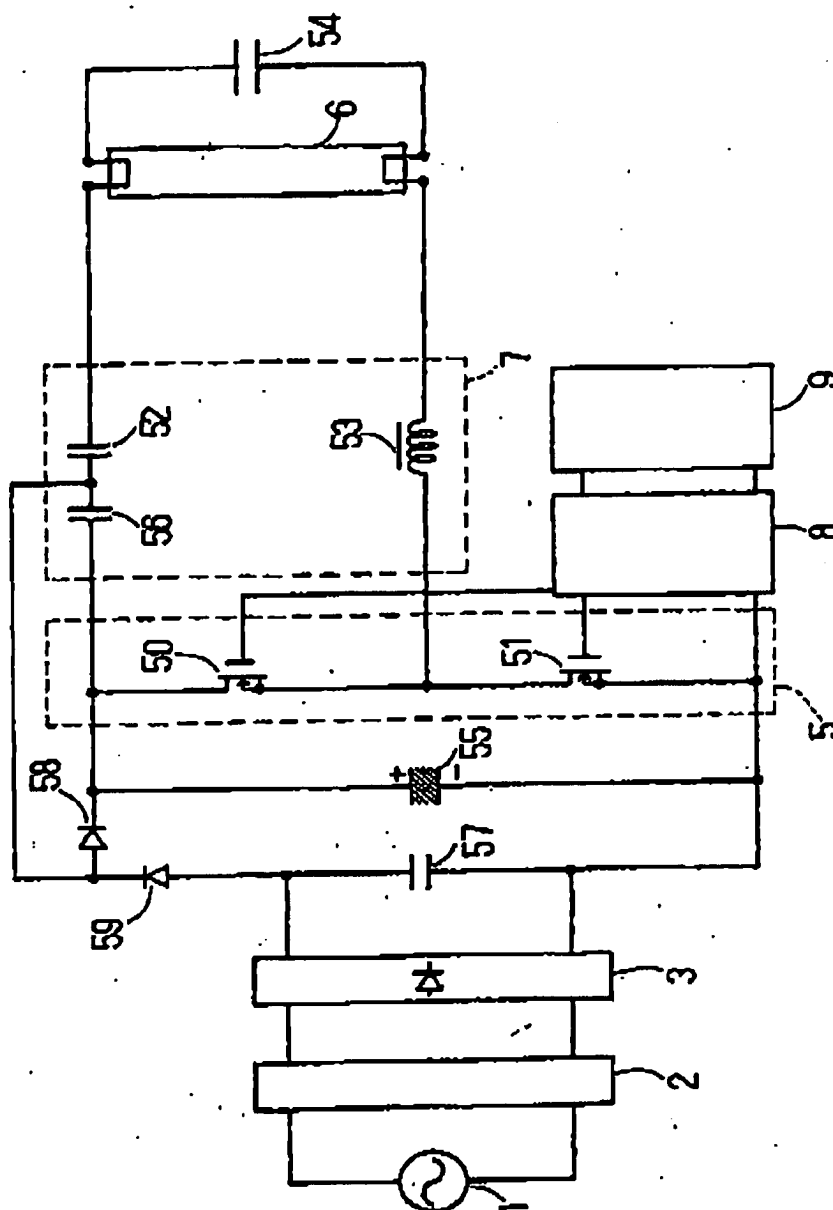


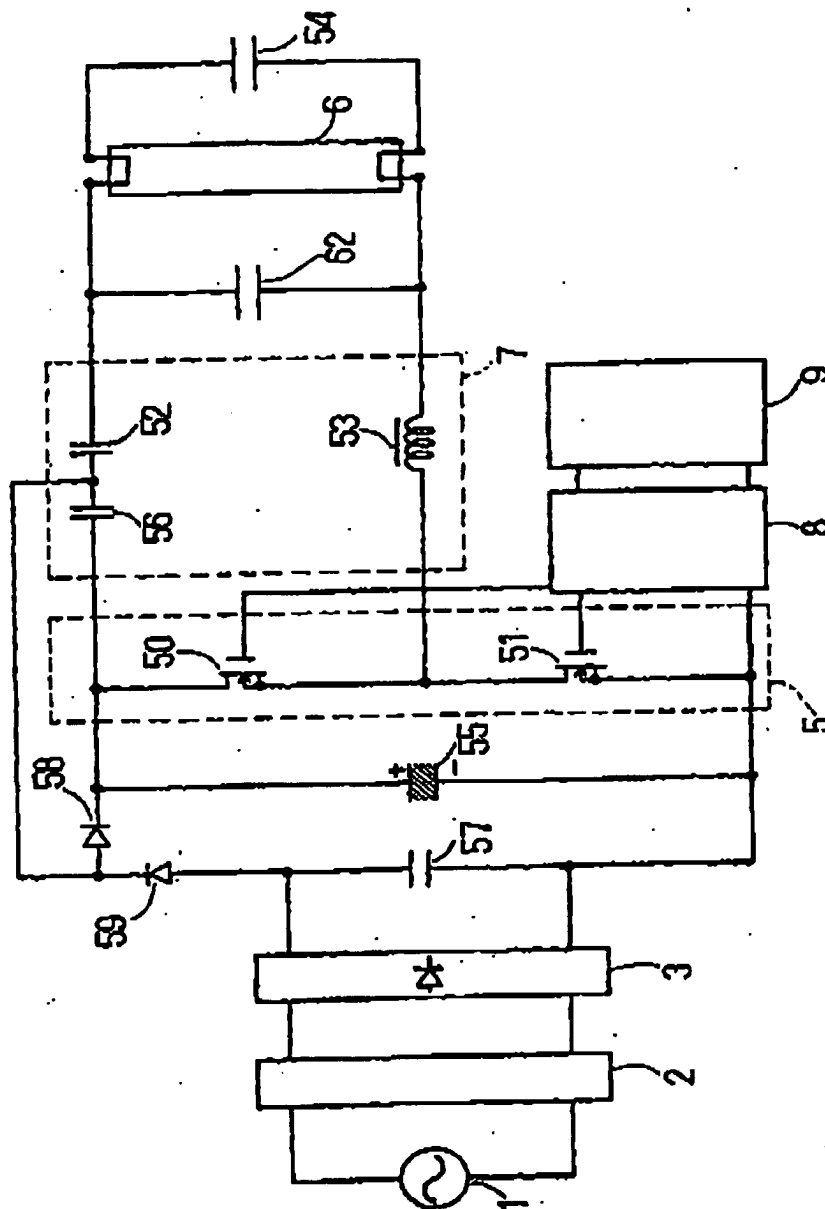
FIG. 2

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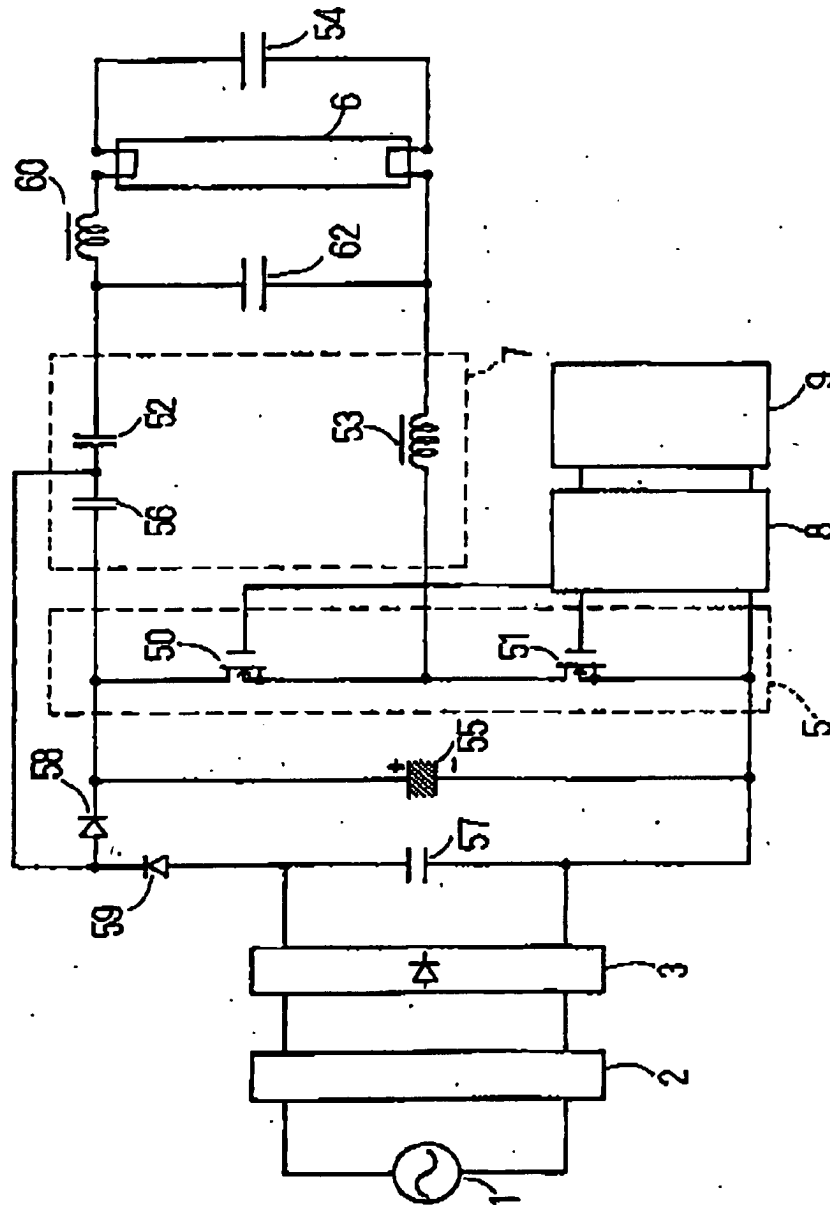


FIG. 4

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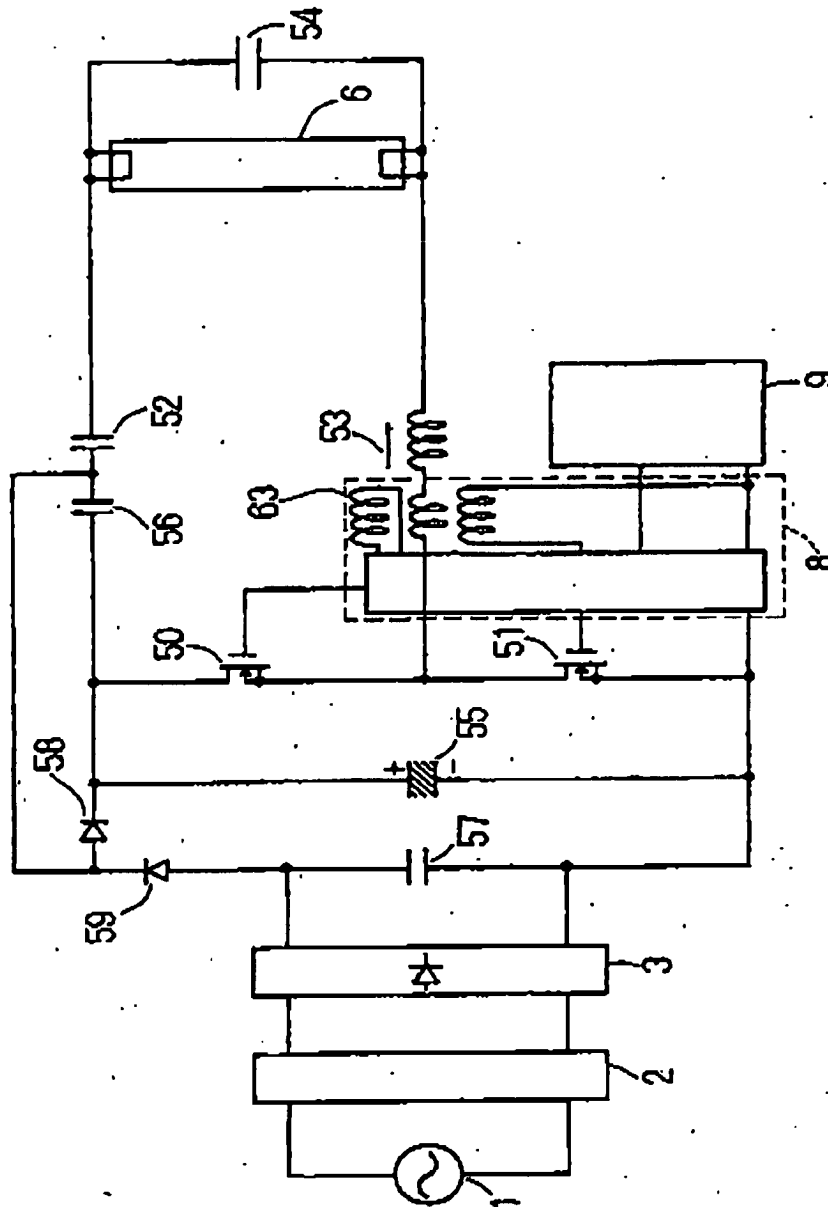


FIG. 5

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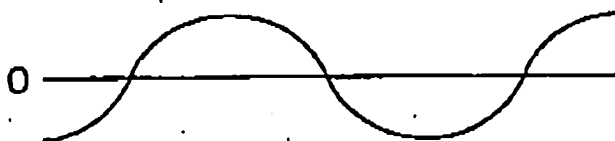


FIG. 6

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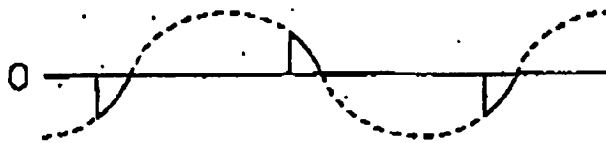


FIG. 7

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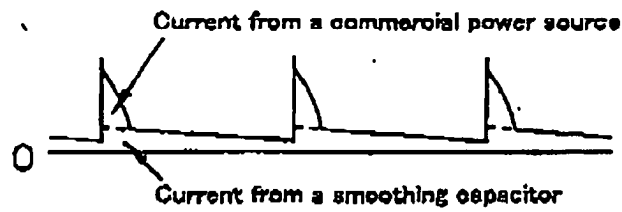


FIG. 8

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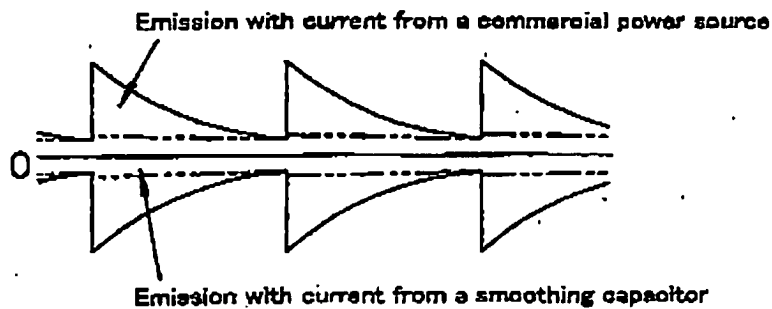


FIG. 9

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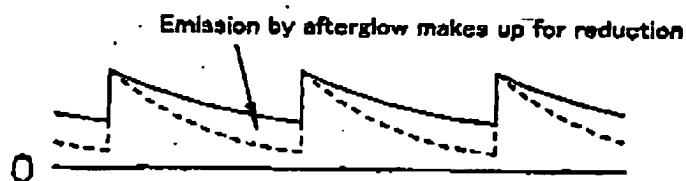


FIG. 10

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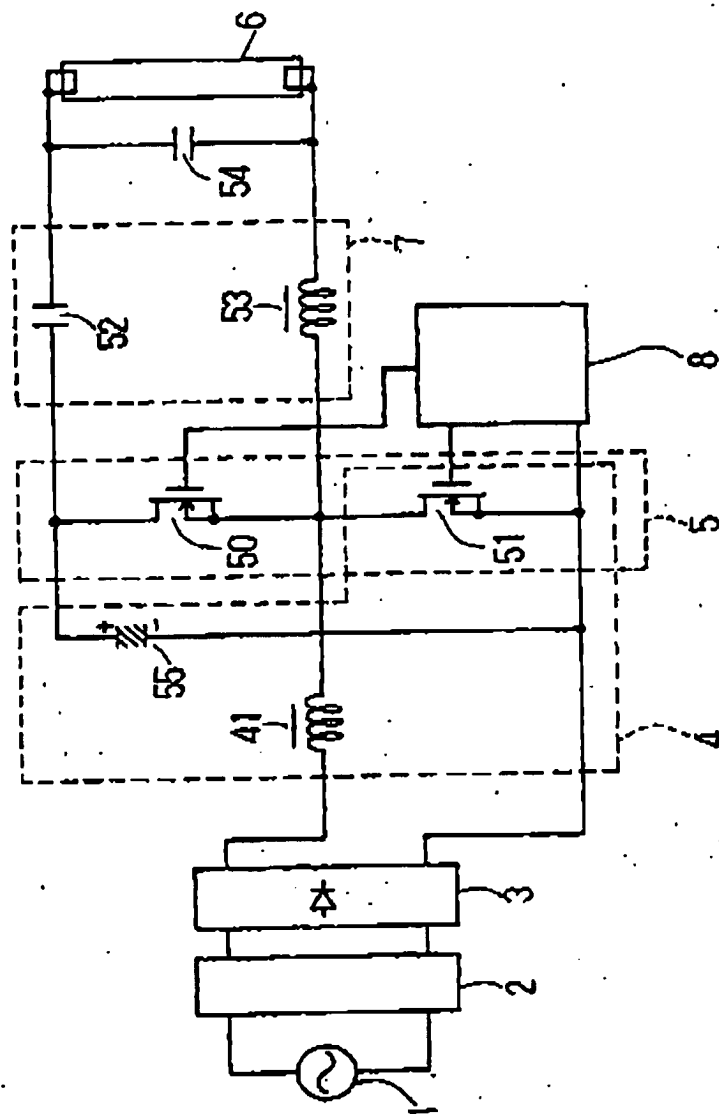


FIG. 11 PRIOR ART

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